

FAULT TOLERANT ELECTRICAL CIRCUIT AND METHOD

ABSTRACT OF THE DISCLOSURE

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An electrical circuit and method substantially to mitigate the effects of a current increase due to a fault within the circuit. In particular, where the electrical circuit includes a laser diode it is desirable to create a fault tolerant circuit to avoid a sudden increase in light intensity output by the laser diode. A track associated with the laser diode is identified and insulated by means of a layout of the circuit. Specifically, where the circuit is an integrated circuit, metal layers and vias are utilised to form an insulating shield around the track associated with the laser diode.

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